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Title of Invention:	INTEGRATED CIRCUITS WITH RAM AND ROM FABRICATION OPTIONS
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1	Petition automatically granted by EFS	TIER-0003_7176713_sb0066e_f iii-- auto_grant_for_Unintentionall y_delayed_payment_of_Main_ Fees.pdf	841478 1fd23e2544efeb8f94e67b6171bb5d53c7 db887	no	3
Warnings:					
Information:					
2	Fee Worksheet (SB06)	fee-info.pdf	32154 ed/b65c5220a7ac17224e6176cbfb7dsb6 d9c18	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				873632	
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